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(54) System and method for a control services link for a multi-shelf node in a communications switch

(57) A connection system for a communication switch is provided. The switch comprises a controller and shelves providing communication capabilities for the switch. The controller is able to communicate with each shelf shelves utilizing categories of communication signals. The connection system comprises connections providing signalling links for the categories of com-

munication signals between the controller and the each shelf. The connections are bundled together in a single, collective span of cable and each connection provides its individual category communication signals independently of the other connections.

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Description

[0001] The invention relates to a system and method providing transmission of control information via control services communication links between components in a communication device.

[0002] Many communication switch and router systems architecture provide modular communication capabilities. Lucent Technologies, Murray Hill, New Jersey has announced a system under its MSC 25000 Multi-service Packet Core Switch (trade-mark of Lucent Technologies). Marconi plc, London, England has announced a system under its BXR 48000 router (trademark of Marconi plc). A common feature of a modular system is to provide separate shelves dedicated to specific tasks.

[0003] In order to have the shelves operate together in the switch, it is necessary to have the shelves communicating control information with amongst themselves. Prior art systems provide the communication links, but there is added complexity in the communication link when the control signals increase in number and complexity. Prior art systems do not manage these signals in an elegant and physically simple manner.

[0004] There is a need for a system and method which improves upon the control signalling systems of the prior art.

[0005] In a first aspect, a connection system for a communication switch is provided. The switch comprises a controller and shelves providing communication capabilities for the switch. The controller is able to communicate with each shelf utilizing categories of communication signals. The connection system comprises connections providing signalling links for the categories of communication signals between the controller and each shelf. The connections are bundled together in a single, collective span of cable and each connection provides its individual category of communication signals independently of the other connections.

[0006] The single, collective span of cable may have a first end having a connector adapted to provide connections for each connection to the controller and a second end having a second connector adapted to provide connections for each connection to each shelf. The first and second connectors may have physical profiles which do not allow the first connector to be connected with the shelf and the second connector with the controller.

[0007] The connection system may have one of the connections adapted to carry E1 frame format signals.

[0008] The connection system may have a second connection adapted to carry Ethernet format signals.

[0009] The connection system may have a third connection adapted to carry RS 485 format signals.

[0010] The connection system may have one of the connections adapted to carry timing synchronization signals for the communication switch.

[0011] The connection system may have the timing

5 synchronization signals comprise a first set of timing signals generated by the controller and transmitted on one of connections to each shelf, a second set of response timing signals generated by each shelf and transmitted on the connection to the controller and a third set of synchronization signals generated by the controller and transmitted on the connection to each shelf. The second set of timing signals is related to local timing signals related to each shelf. The third set of synchronization signals are synchronized to one of the local timing signals.

10 [0012] In other aspects of the invention, various combinations and subset of the above aspects are provided.

15 [0013] The foregoing and other aspects of the invention will become more apparent from the following description of specific embodiments thereof and the accompanying drawings which illustrate, by way of example only, the principles of the invention. In the drawings, where like elements feature like reference numerals (and wherein individual elements bear unique alphabetical suffixes):

Fig. 1 shows a block diagram of a communication network incorporating a switch embodying the invention;

25 Fig. 2A shows another block diagram of components of the switch of Fig. 1;

Fig. 2B shows another block diagram of components and connections of the switch of Fig. 2A;

Fig. 3A shows a block diagram of a cabling system of the switch of Fig. 2A;

30 Fig. 3B shows a block diagram of a RTS signalling circuit associated with an RTS cable associated with the cabling system of Fig. 3A;

Fig. 4 shows a block diagram of a controller card of a control shelf of the switch of Fig. 2A; and

35 Fig. 5 shows a block diagram of a shelf controller card of a high speed peripheral shelf associated with the switch of Fig. 2A.

40 [0014] The description which follows, and the embodiments described therein, are provided by way of illustration of an example, or examples, of particular embodiments of the principles of the present invention. These examples are provided for the purposes of explanation, 45 and not limitation, of those principles and of the invention. In the description, which follows, like parts are marked throughout the specification and the drawings with the same respective reference numerals.

[0015] For general introduction, the following is a description of a network associated with the switch associated with the embodiment. Briefly, the system of the embodiment provides a system for processing data traffic through a routing system or communication switch utilizing a redundant data switching fabric or datapath.

50 [0016] Referring to Fig. 1, a communication network 100 is shown. Network 100 allows devices 102A, 102B, and 102C to communicate with devices 104A and 104B through network cloud 106. At the edge of network cloud

106, switch 108 is the connection point for devices 102A, 102B and 102C to network cloud 106. In network cloud 106, a plurality of switches 110A, 110B and 110C are connected forming the communications backbone of network cloud 106. In turn, connections from network cloud 106 to devices 104A and 104B.

[0017] Switch 108 incorporates the control signal system and method of the embodiment. It will be appreciated that terms such as "routing switch", "communication switch", "communication device", "switch" and other terms known in the art may be used to describe switch 108. Further, while the embodiment is described for switch 108, it will be appreciated that the system and method described herein may be adapted to any switching system, including switches 110A, 110B and 110C.

[0018] Following is a general description of the main elements of a switch for the control signal system and method of the embodiment. Referring to Figs. 2A and 2B, switch 108 is a multi-shelf switching system comprising a number of shelves, including two switching shelves 200A and 200B, switch controller 202 and peripheral shelves 204A ... 204O (providing a total of 15 peripheral shelves). Switching shelves 200A and 200B provide cell switching capacity for switch 108. Switch controller 202 provides input/output for the switch 108. Peripheral shelves 204 provide I/O for switch 108, allowing connection of devices, like customer premise devices (CPEs) 102A, 102B, and 102C to switch 108. Switch controller 202 is a separate shelf with control cards, which provide central management for switch 108. Aspects relating to datastream switching is not provided herein, unless it pertains to a control aspect of switch 108.

[0019] Communication links enable switch controller 202 to communicate with switching shelves 200, peripheral shelves 204. Control Service Links (CSLs) 206 are individual communication links which connect link switch controller 202 with switching shelves 200A and 200B, and peripheral shelves 204 ... 204O. Further detail on CSL links 206 is provided below.

[0020] Terminal 208 is connected to switch 108 and runs controlling software, which allows an operator to modify, and control the operation of, switch 108.

[0021] Referring to Fig. 2B, there are two types of peripheral shelves 204. The first type is a High Speed Peripheral Shelf (HSPS), represented as peripheral shelf 204A. Peripheral shelf 204A contains two redundant High Speed Shelf Controllers (HSC) cards 210. For the data plane, High Speed Line Processing (HLPC) Cards, I/O cards, High Speed Fabric Interface Cards (HFIC) are also provided, but not shown. The second type is a Peripheral Shelf (PS), represented as peripheral shelf 204B. Peripheral Interface Cards 212, Line Processing Cards, I/O cards and Peripheral Fabric Interface Cards (not shown) are provided for the data plane.

[0022] Switch controller 202 comprises a control card 214, a inter-connection (ICON) card 216, ICON- I/O card 218, a Control Interconnect Card (CIC card) 220 asso-

ciated with the control card 214 and a Facilities Card (FAC card) (not shown). The ICON card 216 provides the interface for connecting the switch controller 202 to all peripheral shelf controllers on the other shelves 200, 5 204 in the switch 108. The CIC 220 provides an interface to communicate with line cards in switch controller 202.

[0023] Fig. 2B illustrates CSL 206 connections between various peripheral devices to the switch controller 202. As shown, each high speed peripheral shelf 204B 10 has a pair of redundant shelf controllers 210A and 210B each connecting the peripheral shelf 204 to an ICON I/O interface card 218 via CSL 206. Similarly, each peripheral shelf 204A has a pair of redundant shelf controllers 210A and 210B each connecting the PS 204A to the ICON I/O interface card 218, again via CSL 206. As seen, the embodiment utilizes redundant cards and connections in its architecture.

[0024] Still referring to Fig. 2B, a switching shelf 200A has a shelf controller 222 connecting the switching shelf 200A to ICON I/O interface card 218. Also, a switching shelf 200B has a shelf controller 220B connecting the switching shelf 200B to ICON I/O interface card 218. The connection examples shown in Fig. 2B illustrate the 20 flexibility in the architecture in accommodating many different types of peripheral devices and redundant connections. However, it will be understood the examples shown in Fig. 2B are not limiting. Many alternative designs are possible showing different interconnections.

[0025] Still referring to Fig. 2B, ICON I/O interface card 218A is connected to ICON management card 216. ICON management card 216 is in turn connected to control card 214. Again, the embodiment may utilize redundant cards within switch controller 202 and may connect them to provide multiple paths between each component.

[0026] Aspects relating to the communication of signals between control card 214 to each of peripheral shelf 204, high-speed peripheral shelf 204 and switching shelf 200 is now described.

[0027] As noted above, switch controller 202 provides control operations for switch 108. Control commands are initiated via elements in control card 214 (described later) and are converted to signals which are transmitted to the target shelves.

[0028] It will be appreciated that different control commands will have different bandwidth and timing requirements. In the embodiment, there are four categories of control commands which have different timing sensitivities. The first category of commands has a low bandwidth requirement for its transmission from the switch controller to the destination; however, the commands have a sensitive time requirements for transmission and receipt of the commands. This first category of commands includes commands such as reporting severe 45 faults in a fabric and initiating switching of fabrics. A second category of commands relate to servicing functions for the switch 108. These types of commands may have high bandwidth requirements, but may not have critical 50 55

timing issues. Examples of the second category of commands include commands to download software, update local tables and initiate or tear down a connection. A third category of timing commands include time of day distribution commands. Commands in this third category are used during usage billing calculations. There may not be a critical timing issue for these commands. A fourth category of commands relates to clocking synchronization. It is necessary to synchronize clock signals amongst the switch controller 202 and the peripheral shelves 204A and 204B and the switching shelves 200A and 200B.

[0029] It will be appreciated that each of the four categories of timing commands may be carried over a single control transmission datastream. However, due to the conflicting features of the timing requirements for the categories, it will be appreciated that it may not always be possible to combine all of the four categories together. For example, the high bandwidth, low priority service requirements of the second category of command signals may conflict with the low bandwidth, high priority service requirements of the first category of commands.

[0030] Accordingly, the embodiment provides four separate transmission systems for encoding and transmitting the control signals from the switch controller 202 to the other shelves, namely high speed peripheral shelf 204; peripheral shelves 204B...204O, and switching shelves 200A and 200B. In the embodiment, the following types of signalling protocols are used for each of the four categories of commands:

- (i) for the first category of low bandwidth, high priority commands signals, an E1 communication transmission protocol is used;
- (ii) for the second category of high bandwidth, lower priority commands, an Ethernet signalling transmission protocol is used;
- (iii) for the third category of time of day distribution signalling, a real time signalling (RTS) protocol is used which utilizes electrical characteristics for its signalling per the known RS 485 convention; and
- (iv) for the fourth category of commands, commands are provided via an embedded E1 signalling transmission protocol. The fourth category utilizes features of a known signalling protocol (as will be described later) which is inherent to the E1 communication link used by the first category of commands.

[0031] It will be appreciated that other signalling protocols may be used for any of the four categories of commands, as long as the selected protocols meet the requirements for their respective category, such as timing and bandwidth. It will be also appreciated that when possible, it may be possible to combine one or more of the four categories into a single or multiple set of signals, appropriately modulated on top of each other, to achieve both the transmission requirements for the respective

category and also combining the physical transmissions into one or more communication links.

[0032] Referring to Fig. 3A, for the embodiment, E1 transmissions are carried over two twisted pairs of wire 302A, 302B with one twisted pair 302A carrying communications downstream from the switch controller 202 to the connected shelf and another twisted pair 302B of wire carrying E1 encoded communications from the shelf to the switch controller 202. In the embodiment, 5 the two sets of twisted pairs are grouped together as a set of four conductors twisted together. Similarly, for Ethernet communications, two pairs of twisted wires 310A, 310B are provided from switch controller 202 to the connected shelf, with one pair of wires 310A providing a downstream link from the switch controller 202 to the connected shelf and another twisted pair of wires 310B to carry communications upstream from the connected shelf to the switch controller 202. Again, the two pairs of conductors are grouped together as a set of four conductors twisted together. For RTS signals, a single pair of twisted wires 318 is provided from the switch controller 202 to the connected shelf. As this is a broadcast-type of transmission, no response is required from any of the shelves. Accordingly, no upstream communication link is provided for the RTS signalling. For the fourth category, appropriate signals inherent to the E1 protocol provided as part of the E1 transmission standard for the first category signals are utilized at each end of the existing E1 link.

[0033] Referring to Fig. 3A, each of the distinct transmission protocols, namely E1, Ethernet and RTS, are carried on separate conductors, but the embodiment physically groups together each of the three types of conductors together in a single common link. This link 30 is CSL 206, described earlier. In CSL 206, E1 cable 300 comprises twisted pairs of conductors 302A and 302B. In Fig. 3A, conductors 302A and 302B are illustrated as separate pairs, but as described earlier, the embodiment has the two pairs twisted together. Conductors 302A and 302B are collectively shielded in a metal foil layer 304. Ethernet cable 308 comprises twisted pairs of cables 310A and 310B. Again, pairs 310A and 310B are illustrated, for clarity, as separate pairs. As with E1 cable 300, cables 310A and 310B are surrounded in a metal foil layer 312. The RTS cable 316 comprises a twisted pair of wires 318. In the embodiment, two pairs of conductors twisted together are provided in cable 316; however, for the sake of clarity, only one pair of wires 318 is illustrated in Fig. 3B. Again, twisted pair 318 is surrounded by a metal foil 320. Finally, cables 300, 308 and 316 are collectively surrounded by another metal foil layer 324 and a metal braid 326. An appropriate exterior sheathing may be provided to protect any or all of the cables. In other embodiments, the above noted pairs of conductors may not be twisted together. Accordingly, it will be appreciated that CSL cable 206 individually isolates each cable from its neighbours and collectively isolates the set of cables from RFI and EMI exter-

nal interference. It will be appreciated that cable 206 provides a single run of grouped cables 300, 308 and 316 which are jointly terminated at single connectors at each end of cable 206. Accordingly, cable 206 provides a single connection system for controller 202 to each shelf 204 and 200 which provides all four categories of communication signals. Cable 206 may be terminated at each end with a physically unique connector to prevent miscabling of cable 206 between shelves and in incorrect orientations. It will be appreciated that the term for cable 316 is synonymous with other terms, such as conductor, connection and link.

[0034] Referring to Fig. 3B, further detail on the RTS cable 318 is shown, for an exemplary connection between switch controller 202 and HSPS 204A. To produce the signal that is carried upon RTS link 318, at the interface point of ICON card 218, the original RTS signal carried on line 328 is provided to differential amplifier 330. The voltage level of signals on line 328 is generally 3.3 volts. Differential amplifier produces a differential signal modulating about zero volts. The differential signal is in RS485 format and has a value of approximately 300mV. The differential signal is provided to RTS link 318 on lines 318A and 318B. At HSPS 204A, CSL 206 provides a connection to differential receiver 332. The differential receiver converts the received signal on lines 318A and 318B into a DC signal and outputs the signal on line 334. The output produced on line 334 is a 3.3 volt signal. The signal can then be used by HSPS 204A. For RTS connections for the peripheral switch controller and the switching shelf controller, a comparable RTS connection, as described above, is provided.

[0035] Following is a description of each of the elements in the switch controller 202 and each of the peripheral shelves, 204B, high speed peripheral shelf 204A and switching shelf 200.

[0036] First, a description is provided of each of the elements in each of the shelves related to the E1 transmissions. Referring to Fig. 4, in switch controller 202, control card 214 comprises microprocessor 400 and FPGA 402. ICON card 216 comprises Ethernet switch block 408, control block 410, transceiver block 416 and isolation modules 420. In control block 410, microprocessor 412 operates software which controls ICON card 216. Periodically, the software operating on microprocessor 400 may initiate a command which falls into the first category of commands, described earlier. Accordingly, the software operating in microprocessor 400 sends to FPGA 402 via link 404 a message that a first category command is to be sent. The FPGA 402 generates an appropriate message and sends it via 490 or 492 (depending on the data) to ICON card 216.

[0037] For an E1 command, within ICON card 216, FPGA 414 provides same to control block 410. Control block 410 comprises microprocessor 412 and FPGA 414. Accordingly, an E1 message is generated and the message is provided to transceiver block 416. Within transceiver block 408, the E1 message is routed to E1

framer 418A. E1 framers are known in the art. E1 framer 418A frames the E1 message per the recognized protocol and sends it to external interface block 420. Within external interface block 420, the E1 transmission is received by E1 magnetics and protection block 422A which electrically isolates the signal from the rest of the cable 206.

[0038] Next, the E1 signal is provided to a physical termination point in ICON I/O card 218, namely CSL connector 424. CSL connector 424 has appropriate terminations which allow a connected CSL link 206 to connect its E1 cable 300 to the appropriate terminal in CSL connector 424. From that point, the E1 communications are carried over to the connected shelf by CSL cable 206.

[0039] Referring to Fig. 5, elements relating to the processing of the E1 communication protocol are shown for high speed peripheral shelf 204A. First, CSL link 206 connects to external interface block 500 at CSL connector 502. CSL connector 502 provides an appropriate connection from CSL cable 206 to the interface portion for E1 transmission protocol processing for high speed peripheral shelf 204A. Accordingly, from CSL connector 504, E1 signals are provided to E1 magnetics and protection block 504A which provides electrical isolation of high speed peripheral shelf 204A from E1 signals carried upon CSL link 206. From external interface block 500, the E1 communications stream is provided to transceiver block 506. Within transceiver block 506, the E1 signals are received by E1 framer 508A which appropriately unframes the E1 signal. The unframed E1 signal is then provided to control block 510. Therein, microprocessor 512 and FPGA 514 collectively operate to decode the E1 signal to identify the message contained therein and the destination of the message into an appropriate DC signal, providing a midplane signal which can be provided to line card 516.

[0040] In the embodiment, a toggling signal is generated and sent by a shelf controller to indicate its activity status to its associated line card. The activity status of a shelf controller is either active, inactive, a request to be active or a fault. A request to be active is typically generated on startup of the switch. In particular, in a shelf controller the toggling signal is generated by FPGA 514. For example, if the shelf controller is being switched from an active state to an inactive state, the signal provided by FPGA 514 to line card 516 is a "slow toggle" signal. If the shelf controller is being switched from an inactive state to an active state, the signal provided by FPGA 514 to line card 516 is be a "fast toggle" signal. If a "medium toggle" signal is generated by FPGA 514, a request to be active is sent from the shelf controller. If the signal is a DC value, i.e. no toggle, then an error condition is present in the shelf controller.

[0041] In the embodiment, the protocol parameters of E1 communications enables time sensitive commands to be encoded and sent from switch controller 202 to various remote shelves.

[0042] It will be appreciated that communications

from line card 516 destined for switch controller 202 which are encoded in E1 format may be processed by the earlier described modules for controller 202 in a similar fashion, as described above but for the flow of data occurring in the upstream direction. Upstream E1 transmissions are carried on CSL link 206 via cables 302B.

[0042] Following is a description of Ethernet communications from switch controller 202 to line card 516. Referring to Fig. 4, software operating on microprocessor 400 determines that a download of updated software, for example, is necessary for line card 516. Microprocessor 400 sends a message directly to ICON card 216. In ICON card 216, the Ethernet message is received at Ethernet switch 408 and provided directly to the Ethernet PHY block 418B of transceiver block 416. From transceiver block 416, the Ethernet encoded message is provided to external interface block 420. Therein, the signal is provided to Ethernet magnetics and protection block 424B. Therefrom, the Ethernet signal is provided to CSL connector 424 which has appropriate connections to CSL cable 206 to cables 308. From that point, the Ethernet message is sent over CSL 206 to the connecting HSPS 204A.

[0043] Referring to Fig. 5, the Ethernet message in CSL 206 is received at external interface block 500 at CSL connector 502. The Ethernet stream is directed to Ethernet magnetics and protection block 504B. Therefrom, the Ethernet message is provided to transceiver block 506 and Ethernet PHY conversion block 508B. From PHY block 508B, the Ethernet message is provided to Ethernet switch block 516. Ethernet switch block 516 routes the Ethernet message to midplane interface block 518 which then provides the Ethernet signal to line card 516.

[0044] It will be appreciated that communications from line card 516 to switch controller 202 which are carried over our Ethernet link may be processed by the above described Ethernet modules in a similar fashion as described above, but for the flow of data occurring in the upstream direction. Ethernet transmissions are carried over CSL link 206 via cables 310B. Ethernet messages may also be provided to local microprocessors in ICON card 216 and shelf controller 210.

[0045] In the embodiment, it will be appreciated that Ethernet transmissions may be transmitted from its source directly to the line card 516. Whereas with E1 communications, in the embodiment it is not possible to send some transmissions from ultimate source to ultimate destination without having an intervening protocol introduced to transmit the message to the "last mile", i.e. the component. It will be appreciated that in other embodiments, other modules may be provided to allow "end-to-end" transmissions of any given protocol for any given signal.

[0046] The following is a description of the transmission of RTS signals from switch controller 202 to line card 516. The RTS signalling system provides a time stamp to all line cards 516. The RTS pulse has an em-

bedded date value. The RTS signal allows a local card to synchronize with a clock using an offset value contained in the signal. The time stamp provides 100 ms of synchronization accuracy between cards 516 on different shelves.

[0047] To accomplish this, two counters are used in RTS receivers to generate a universal time stamp: a local high resolution (1 us) counter and a low resolution counter. The low resolution signal is sent over the RTS link 316. The receipt of the low-resolution signal provides a stimulus to initiate a free-running counter which is used to generate the high-resolution portion of the timestamp. For the low-resolution counter, it has a value derived from a PWM signal generated from the switch controller 202. The sequence of values in the low resolution counter is a pseudo-random number and is provided in an accessible software table. Accordingly, a look-up must be performed at line card 516 to determine the offset time indicated by the low resolution counter. At line card 516 the result of this look-up is combined with the local high resolution counter to provide a local time stamp having micro-second precision.

[0048] Referring to Fig. 4, for a time stamp signal, software operating on microprocessor 400 in switch controller 202 sends a base time signal on Ethernet link 494 to ICON card 216. FPGA 402 generates a pulse width modulation (PWM) signal encoding the RTS message and provides same to ICON module 216 via link 492. Within ICON module 216, the RTS message is received at control block 410. Therein FPGA 414 cause the generation of a broadcast signal which will be "fanned out" to each of the connected HSPS cards for 204A. Accordingly, in "fanning out" the signal, control block 410 provides the received RTS message to RS 485 transmitter 422C in transceiver block 416. From transceiver block 416, the message is provided to external interface block 420 and RS 485 isolation and protection module 422C. From there, the isolation block 422C has a connection to the appropriate RTS links in CSL connector 424. Accordingly, the RTS signal is carried upon appropriate CSL cable(s) 206 and wires 316 to each shelf.

[0049] Referring to Fig. 5, at a particular shelf the RTS message is received from CSL cable 206 at CSL connector 502 in external interface block 500. From the connector 502, the RTS signal is provided to RS 485 isolation and protection block 504C which electrically isolates the HSPS 204A from CSL 206. From external interface block 500, the RTS signal is provided to transceiver block 506. In transceiver block 506, the signal is provided to RS 485 receiving block 508C and then, the RTS signal is provided to control block 510. Therein, FPGA 514 generates signals to distribute the RTS signal locally to each connected line card 516 or other elements which have a microprocessor.

[0050] The following is a description of the fourth category of commands. As noted earlier, the embodiment provides synchronization for all clocks distributed in the system, namely the controller clocks on the switch controller 202 and the each of the shelves. The following

signalling method is used which is transmitted by piggy backing the signals on the E1 communications stream.

[0050] First, a description of the general parameters of the timing mechanism is provided. As each of the I/O sources are operating on different SONET rings, the embodiment provides a method of synchronizing all timing aspects amongst each of the rings. Essentially, the switch controller 202 receives timing information from each of the distributed elements, namely line cards 516 etc. Initially an arbitrary clock signal is generated by the switch controller 202 as the synchronization signal. Next, the peripheral shelf 204 recovers the timing signal from the E1 channel driven by switch controller 202. Next, shelf controller 204 calculates a digital phase word and places the word in the TDM stream, which is transmitted to switch controller 202. Then switch controller 202 notifies shelf controller 204 of which line card 516 is to be used to calculate the digital phase word. Finally, ICON management card 216 utilizes the clock signal provided by switch controller 202 to synchronize E1 framers such that all shelf controllers 204 and 200 receive the same synchronization signal.

[0051] It will be appreciated that the embodiment provides a single connection point between heterogeneous cabling systems having common beginning and termination points. Also, the embodiment provides a system of transmitting heterogeneous signalling protocols to various shelves. It will further be appreciated that the embodiment may be used between any shelves or components in the switch requiring a plurality of signalling systems.

[0052] It is noted that those skilled in the art will appreciate that various modifications of detail may be made to the present embodiment, all of which would come within the scope of the invention.

Claims

1. A connection system for a communication switch comprising a controller and a plurality of shelves providing communication capabilities for said communication switch, said controller being able to communicate with each shelf of said plurality of shelves utilizing a plurality of categories of communication signals, said connection system comprising:

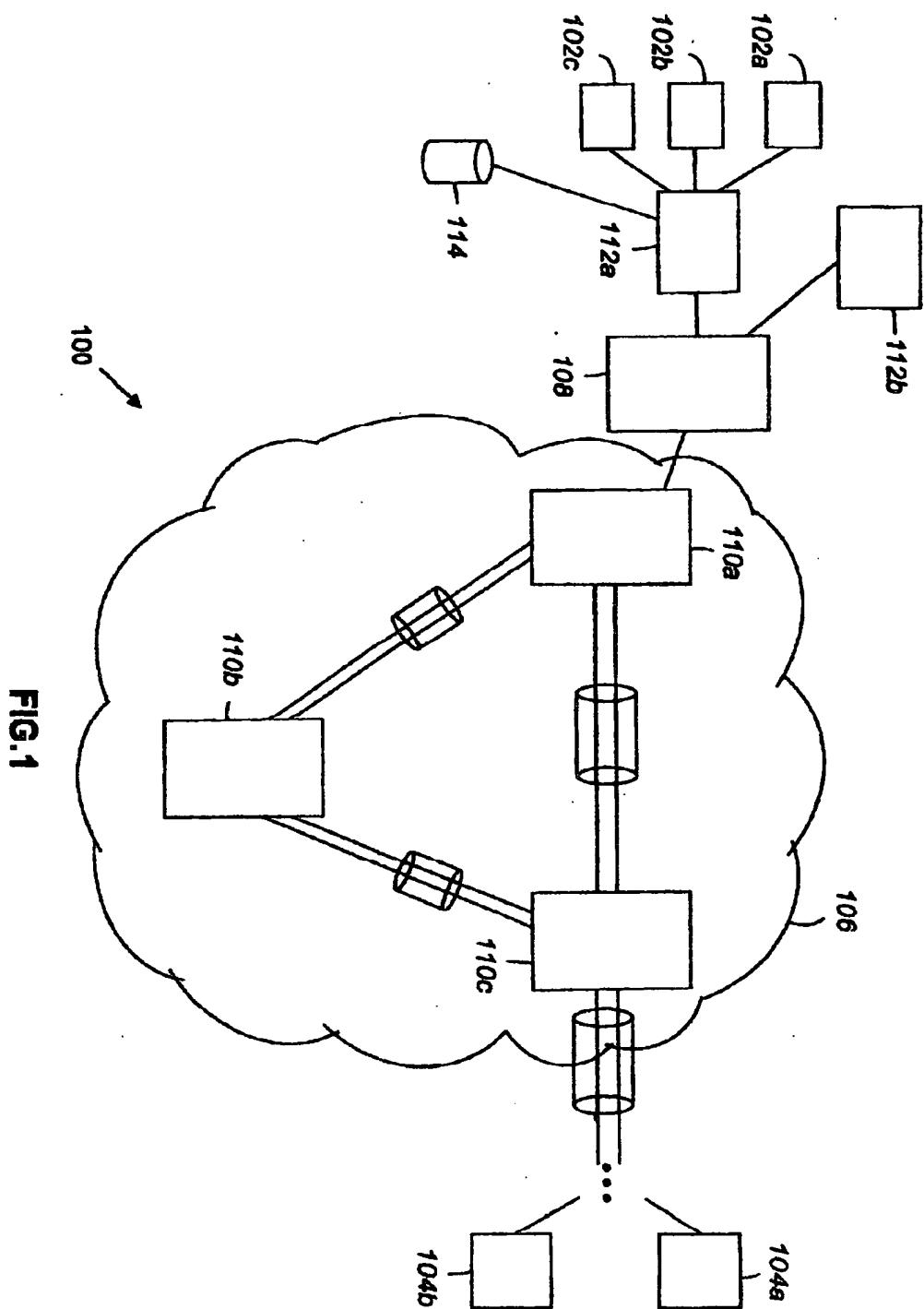
a plurality of connections providing signalling links for said plurality of categories of communication signals between said controller and said each shelf of said plurality of shelves,

wherein said plurality of connections are bundled together in a single, collective span of cable and each connection of said plurality of connections provides its individual category of said plurality of categories of communication signal independently of

the other connections of said plurality of connections.

2. A connection system according to claim 1 wherein said single, collective span of cable comprises a first end having a connector adapted to provide connections for each connection of said plurality of connections to said controller and a second end having a second connector adapted to provide connections for each connection of said plurality of connections to said each shelf; and wherein said first and second connectors have physical profiles which do not allow said first connector to be connected with said shelf and said second connector with said controller.
3. A connection system according to any one of claims 1 or 2 wherein one of said plurality of connections is adapted to carry E1 frame format signals.
4. A connection system according to claim 3 wherein a second of said plurality of connections is adapted to carry Ethernet format signals.
5. A connection system according to claim 4 wherein a third of said plurality of connections is adapted to carry RS 485 format signals.
6. A connection system according to any one of claims 1 to 5 wherein one of said plurality of connections is adapted to carry timing synchronization signals for said communication switch.
7. A connection system according to claim 6 wherein said timing synchronization signals comprise:

a first set of timing signals generated by said controller and transmitted on said one of said plurality of connections to each of said plurality of shelves; a second set of response timing signals generated by each of said plurality of shelves and transmitted on said one of said plurality of connections to said controller, said second set of timing signals related to local timing signals related to each of said plurality of shelves; and a third set of synchronization signals generated by said controller and transmitted on said one of said plurality of connections to each of said plurality of shelves, said third set of synchronization signals synchronized to one of said local timing signals.



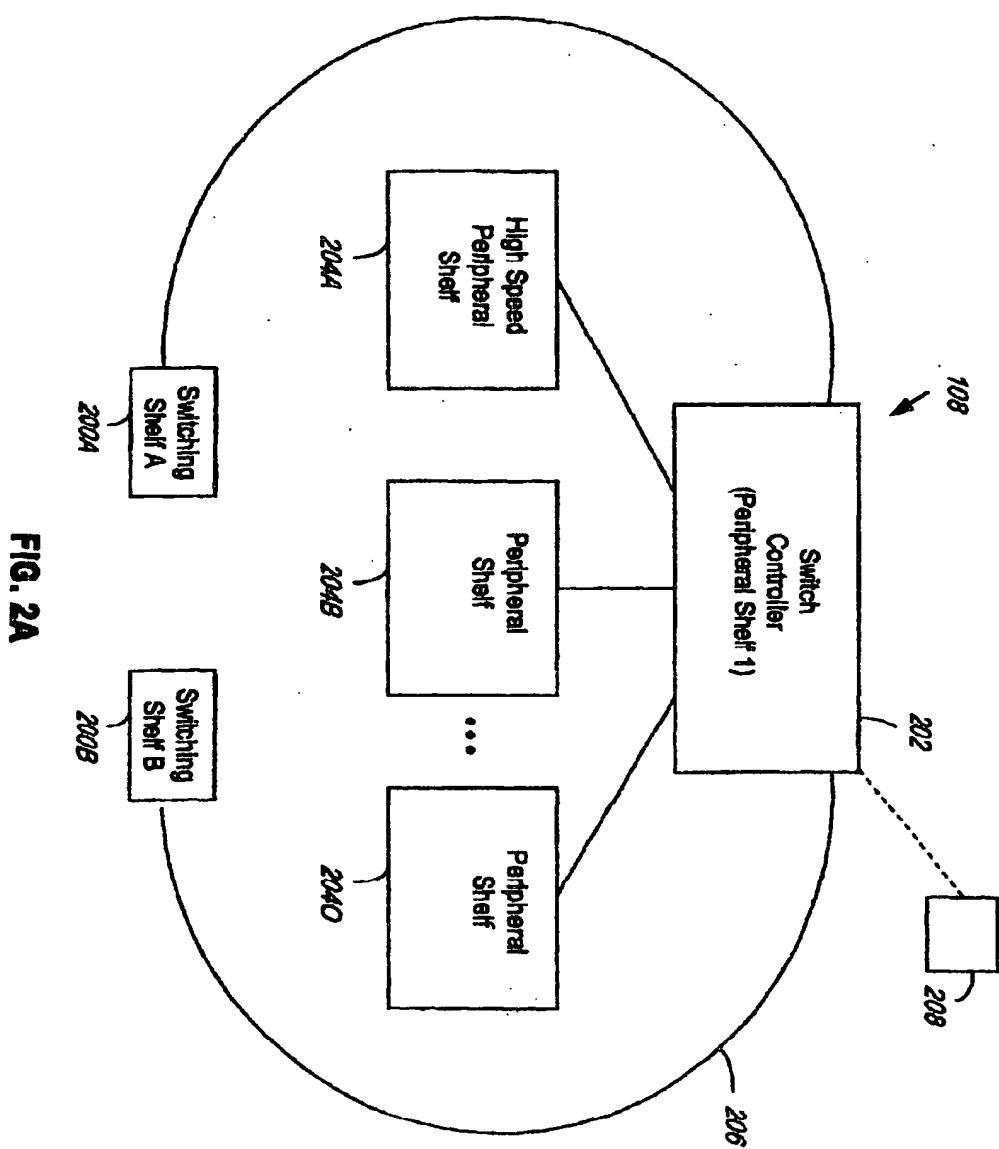


FIG. 2A

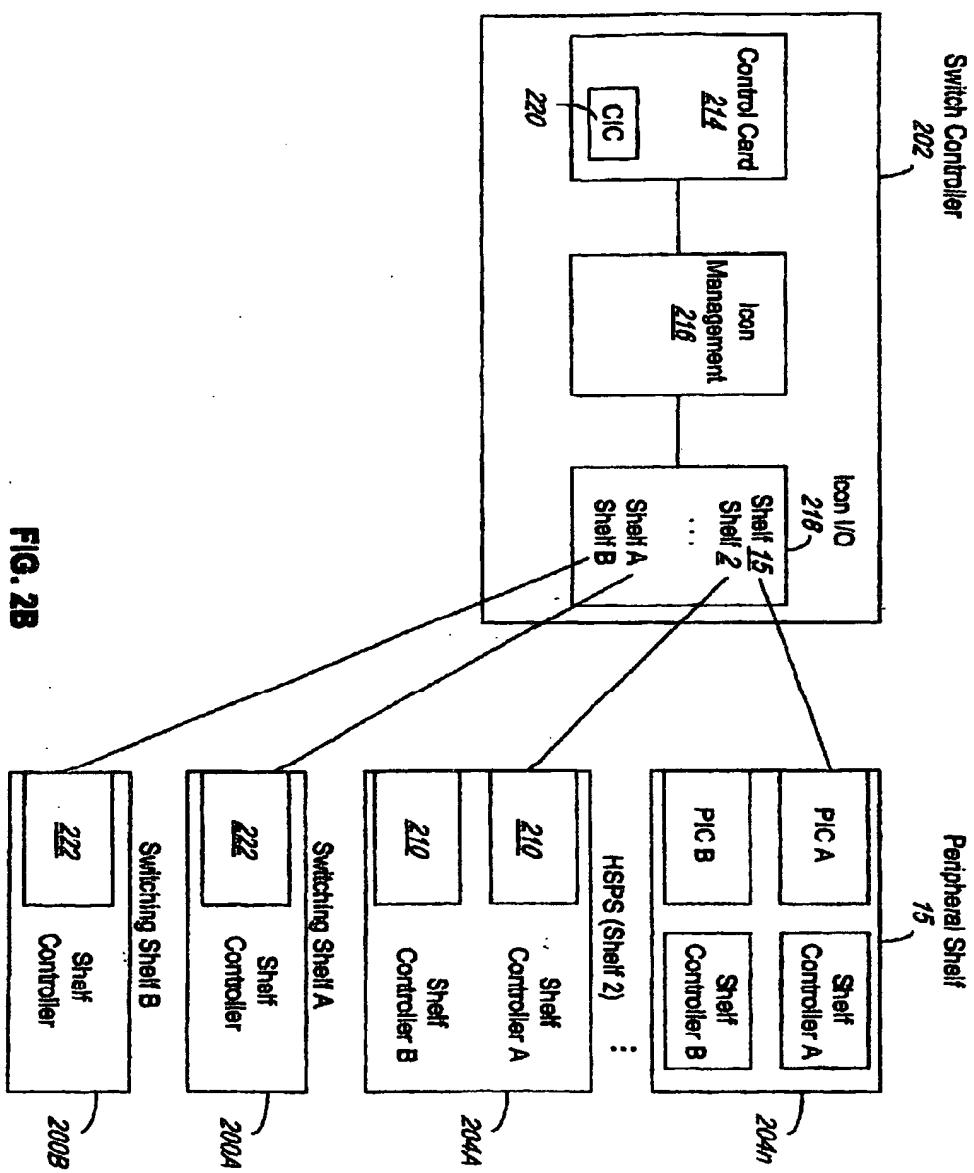
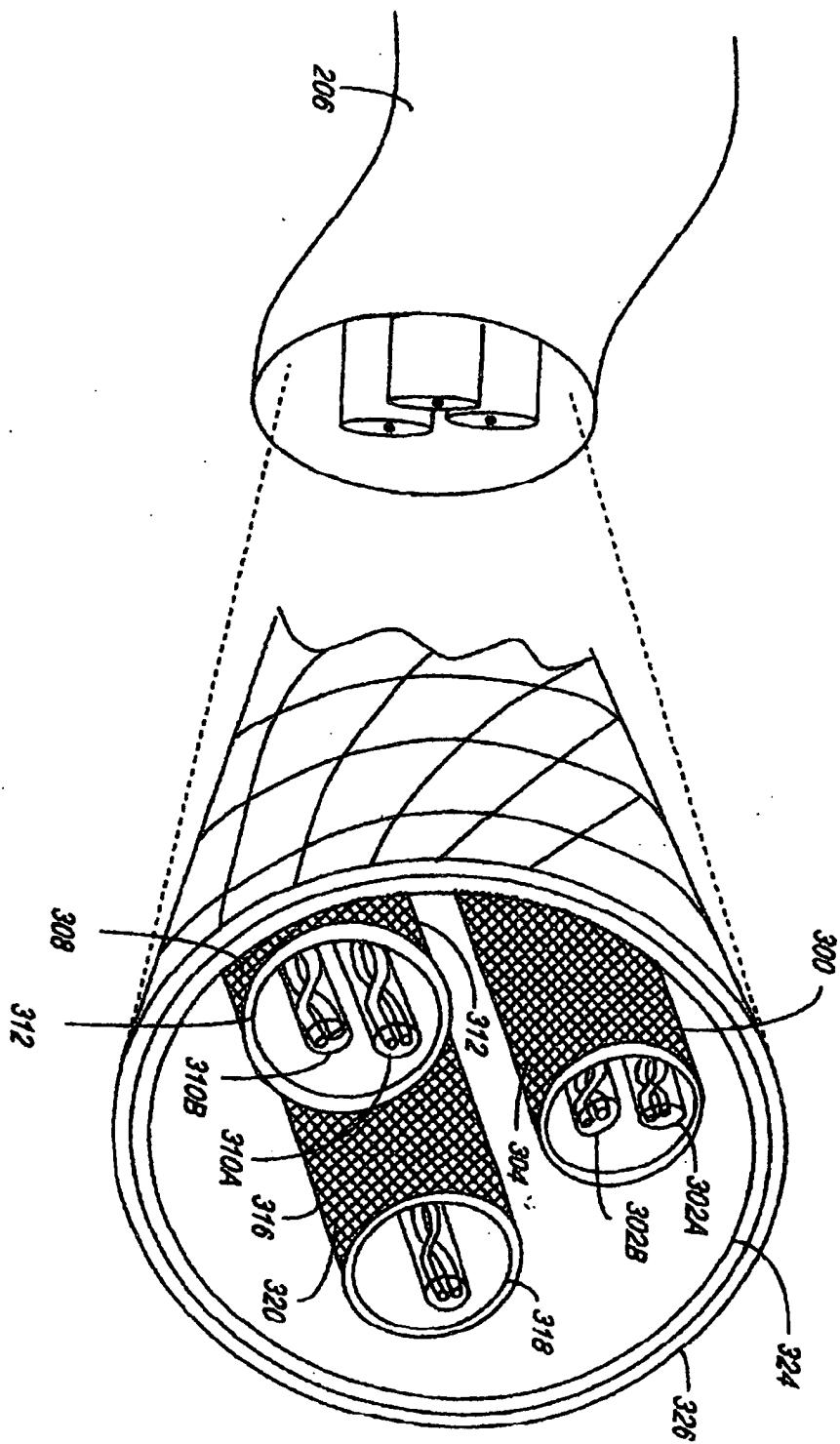
**FIG. 2B**

FIG. 3A



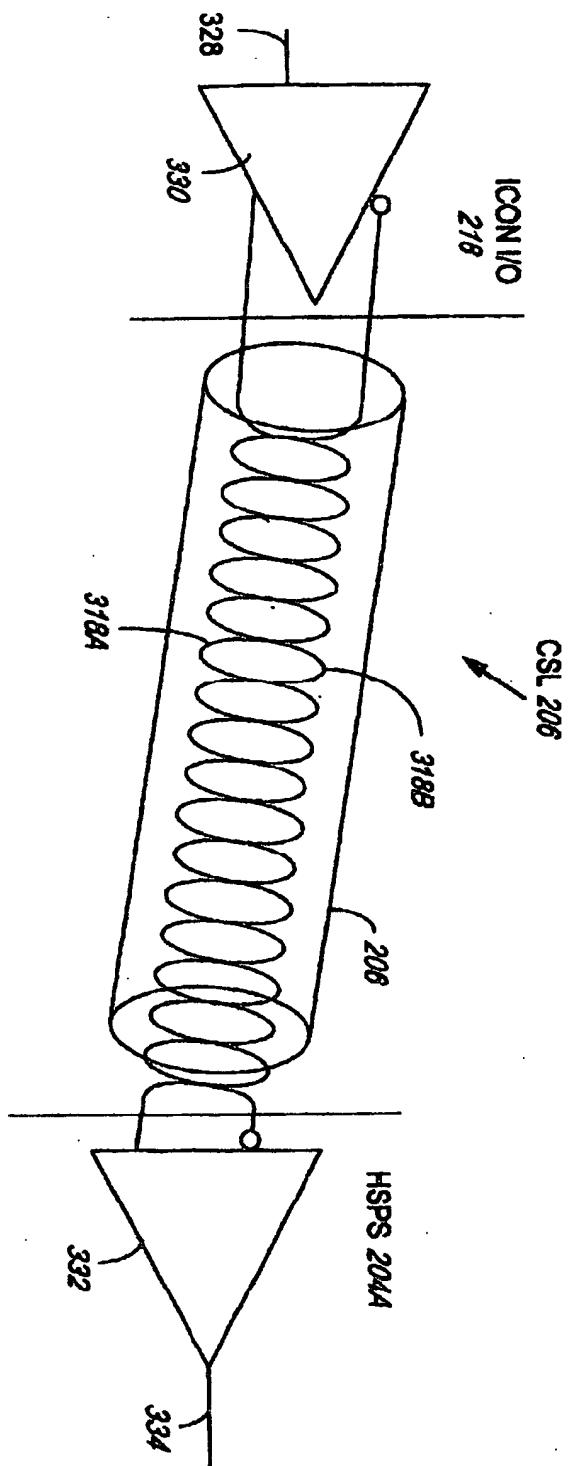


FIG. 3B

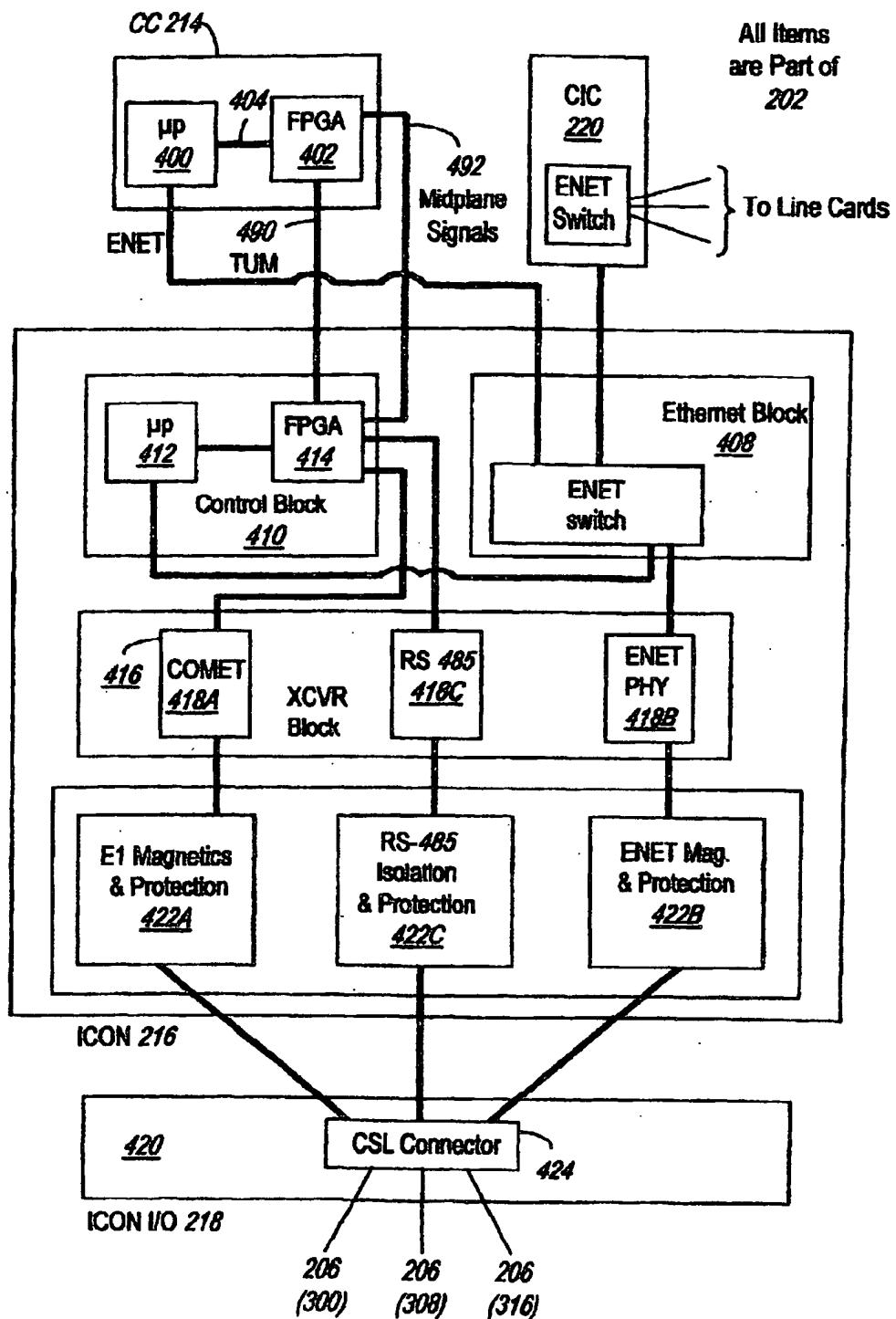


FIG. 4

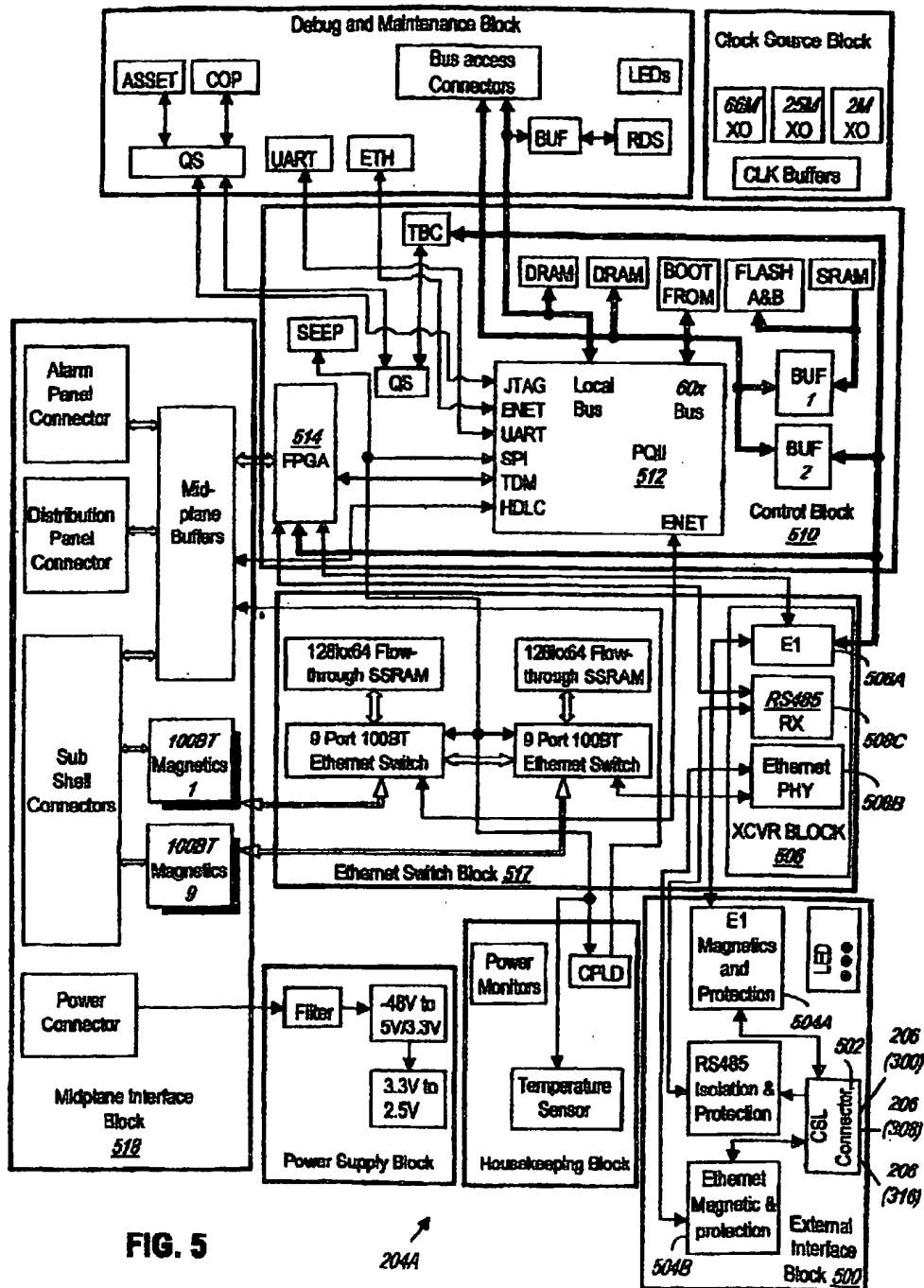
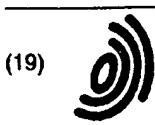


FIG. 5



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(12) **EUROPEAN PATENT APPLICATION**

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(54) **System and method for a control services link for a multi-shelf node in a communications switch**

(57) A connection system for a communication switch is provided. The switch comprises a controller and shelves providing communication capabilities for the switch. The controller is able to communicate with each shelf shelves utilizing categories of communication signals. The connection system comprises connections providing signalling links for the categories of com-

munication signals between the controller and the each shelf. The connections are bundled together in a single, collective span of cable and each connection provides its individual category communication signals independently of the other connections.

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EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
A	US 5 729 546 A (GUPTA AMAR ET AL) 17 March 1998 (1998-03-17) * column 1, line 7 - line 11 * * column 2, line 26 - column 3, line 4 * * column 4, line 16 - column 5, line 7 * * figure 1 *	1-7	H04L12/56
A	EP 0 687 123 A (HARRIS CORP) 13 December 1995 (1995-12-13) * page 2, line 1 - line 2 * * page 2, line 10 - line 12 * * page 3, line 4 - line 12 * * page 4, line 13 - line 25 * * page 9, line 38 - page 10, line 3 * * page 10, line 33 - page 11, line 10 * * figures 5-9 *	1-7	
A	US 3 997 879 A (STRUGER 000 J ET AL) 14 December 1976 (1976-12-14) * column 1, line 1 - line 3 * * column 2, line 15 - line 21 * * column 3, line 1 - line 17 * * column 5, line 24 - line 27 * * figure 1 *	1-7	TECHNICAL FIELDS SEARCHED (Int.Cl.)
A,P	US 6 421 322 B1 (SIT ERIC KAR-WING ET AL) 16 July 2002 (2002-07-16) * column 1, line 7 - line 10 * * column 6, line 24 - line 58 * * column 7, line 48 - line 50 * * column 8, line 13 - line 25 * * column 9, line 4 - line 30 * * figures 3-5 *	1-7	H04L
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
MUNICH	17 March 2004	Schrembs, G	
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone		T : theory or principle underlying the invention	
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P : intermediate document		& : member of the same patent family, corresponding document	

ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

EP 02 29 2375

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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17-03-2004

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
US 5729546	A	17-03-1998	AU CA EP WO	6286196 A 2217693 A1 0838124 A1 9701257 A1	22-01-1997 09-01-1997 29-04-1998 09-01-1997
EP 0687123	A	13-12-1995	US US CA CN CN EP JP	5748627 A 5583856 A 2151293 A1 1351436 A 1129874 A ,B 0687123 A2 8056394 A	05-05-1998 10-12-1996 11-12-1995 29-05-2002 28-08-1996 13-12-1995 27-02-1996
US 3997879	A	14-12-1976	CA DE GB GB	1067187 A1 2658313 A1 1531160 A 1531159 A	27-11-1979 07-07-1977 01-11-1978 01-11-1978
US 6421322	B1	16-07-2002	AU CA EP US WO	1413599 A 2308473 A1 1033039 A1 2002071394 A1 9926426 A1	07-06-1999 27-05-1999 06-09-2000 13-06-2002 27-05-1999